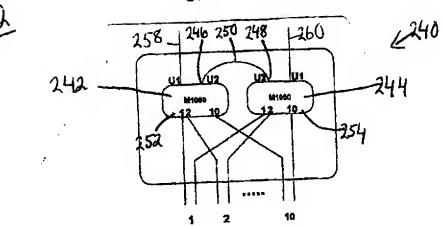
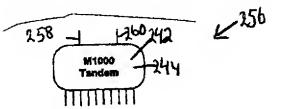
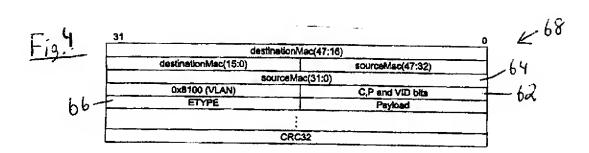


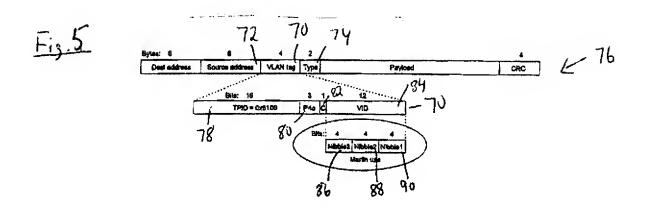
Fig.2



Fis.3







Address	Description	
0x0	This level not addressed	. 92
0x1-0xA	Address for ports 1-10	1/
0xB-0xD	Reserved	<u>د ـ</u> ــ
0xE	Address for AUX post	
0xF	Address for ring topology detection	
Untagged	Address for OAM in unit	

Fig.7

Nibble	Valid values
Nibble 3	0x0-0xA, non zero only if Nibble 2 is non zero
* ASPIDITO T	UXU-UXA
Nibble 1	0x1-0xA, 0xE, 0xF

3 Kai

Fig. 8 Second nibble Address First nibble 0x0This level not addressed This level not addressed Port 1-10 0x1-0xA Ring node 1-10 Reserved Ring node 11-12 0xB-0xC Reserved 0xDRescryed Address for AUX port 0xE Reserved Address for topology detection Reserved 0xF Address for OAM in unit Reserved Untagged

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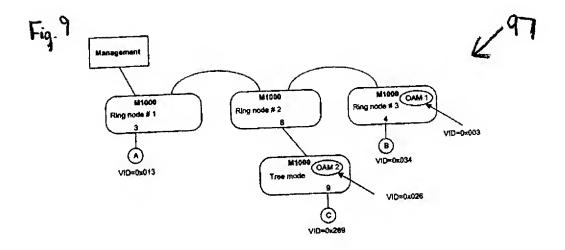


Fig. 10

Nibble Valid values

Nibble 3 0x0-0xC, non zero only if Nibble 2 is non zero,

0xB or 0xC only if this field is ring node number

Nibble 2 0x0-0xC, 0xB or 0xC only if this field is ring node number

Nibble 1 0x1-0xA, 0xE, 0xF

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